

Fig. 1

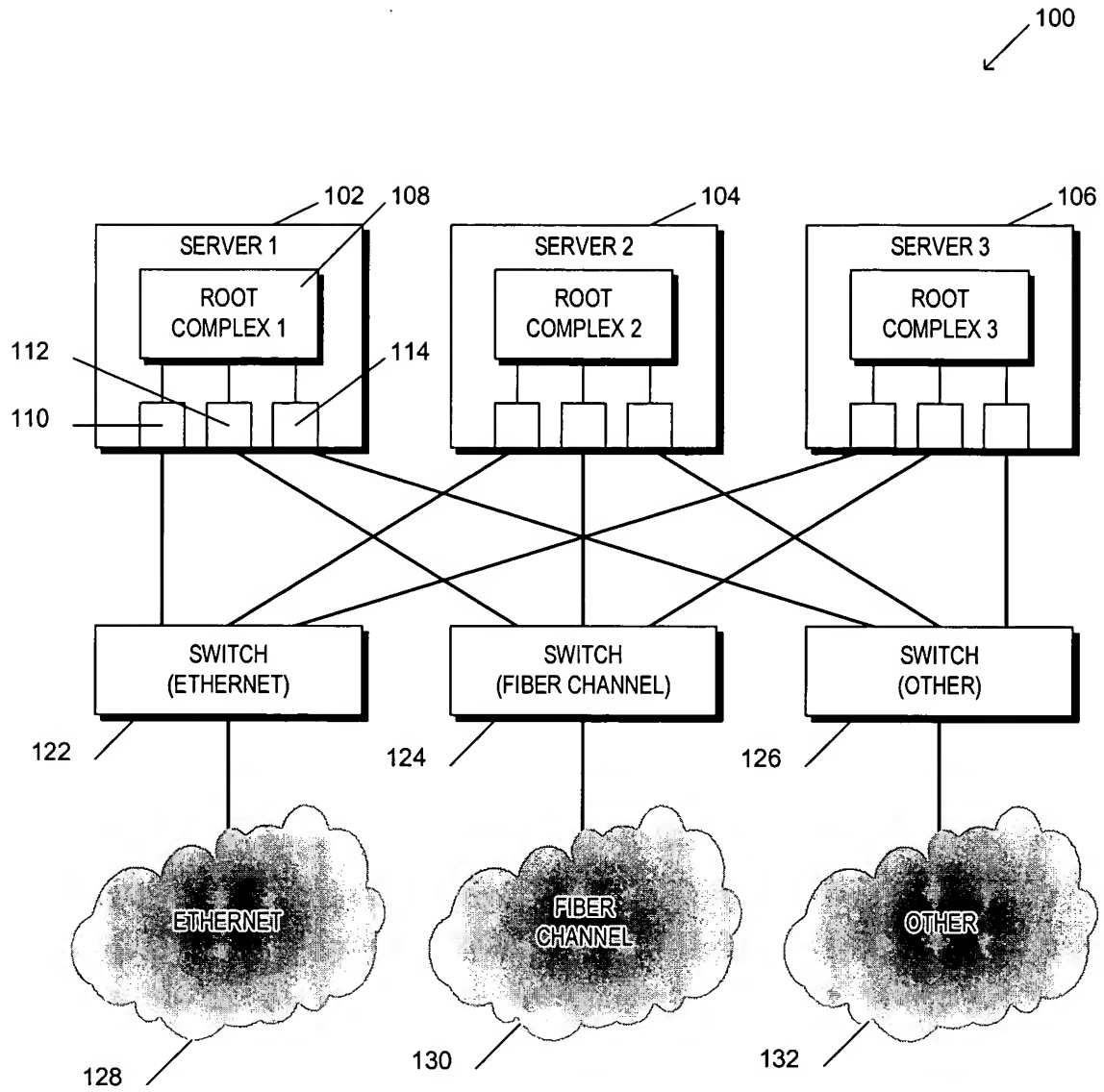


Fig. 2A

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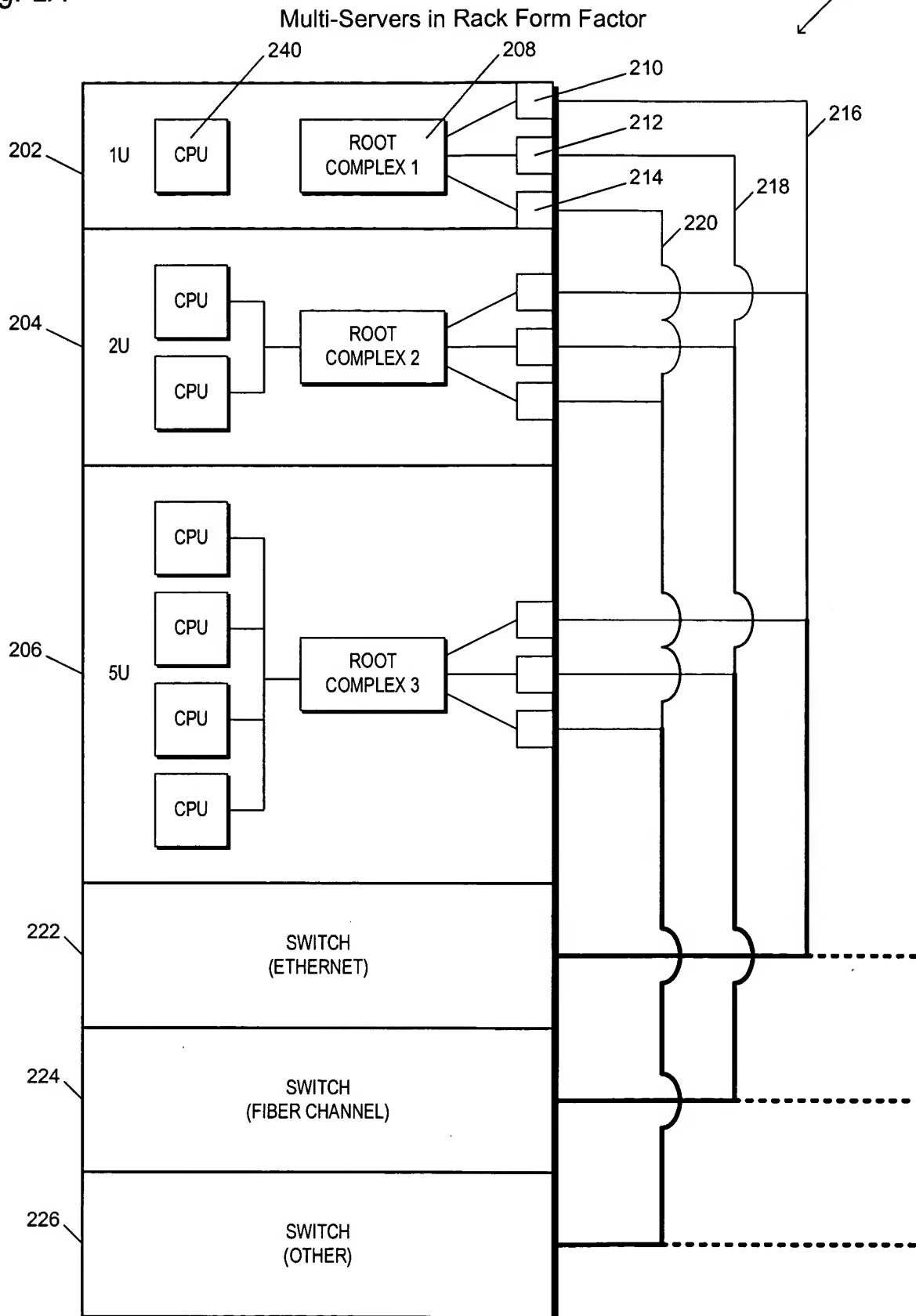


Fig. 2B

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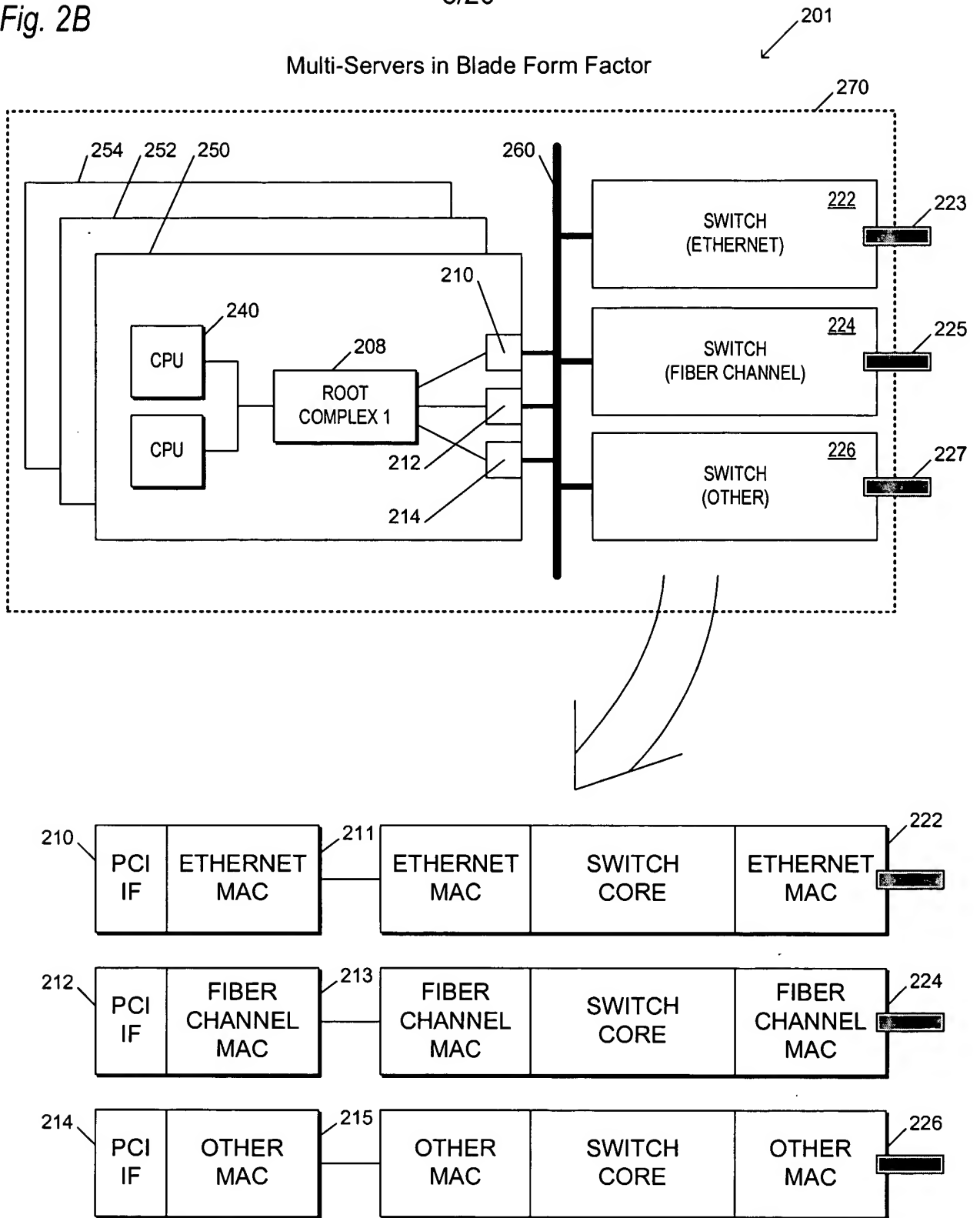


Fig. 2C

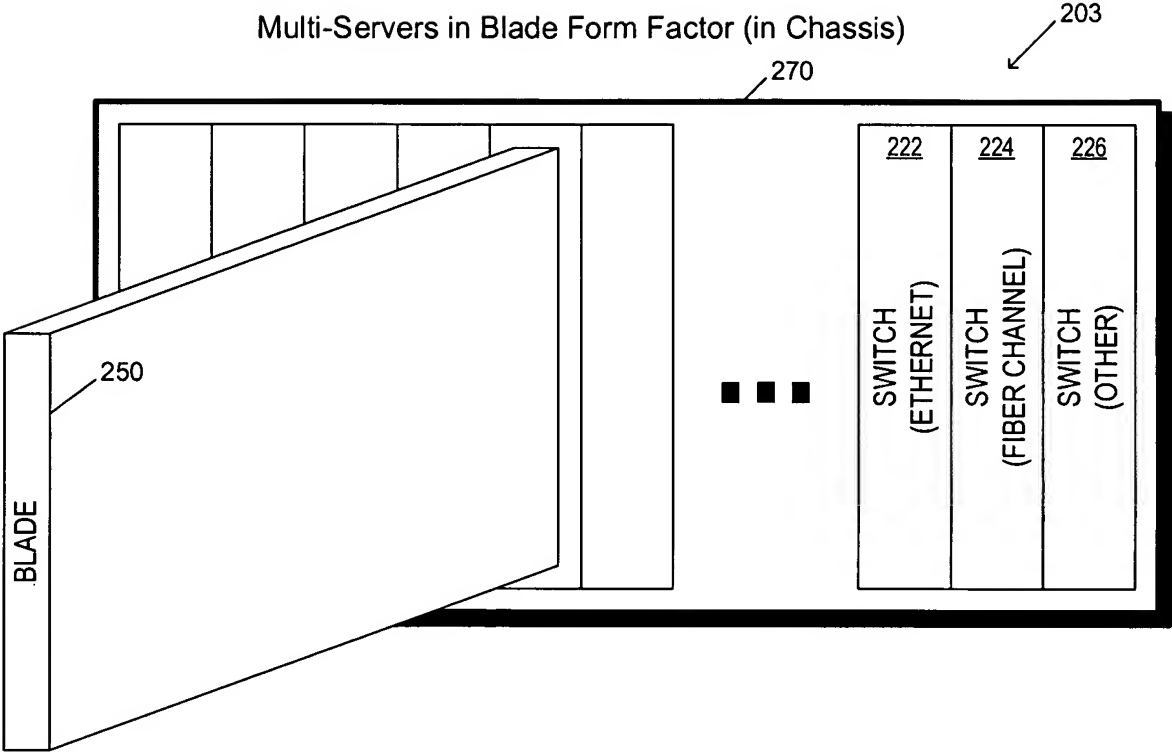


Fig. 3

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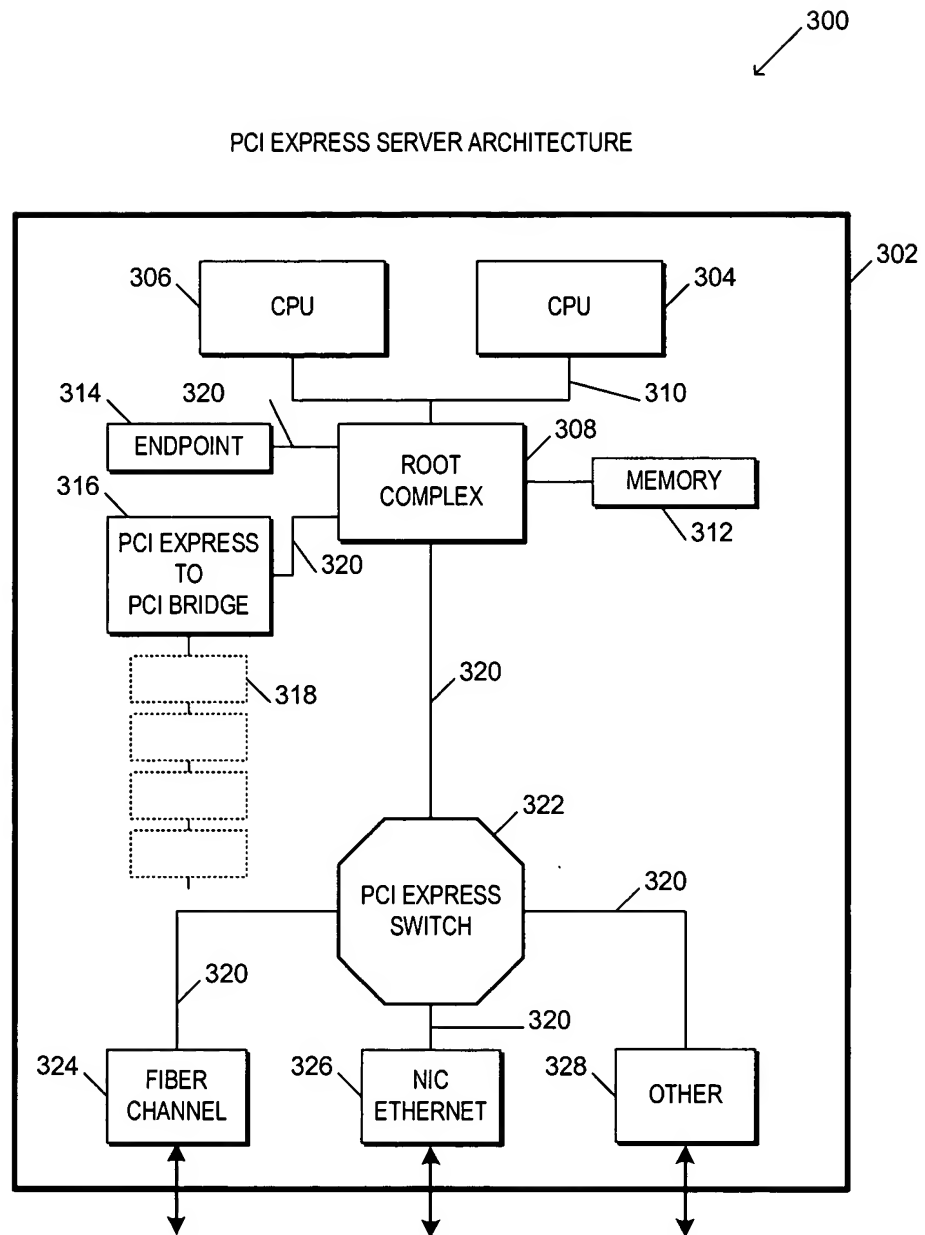


Fig. 4

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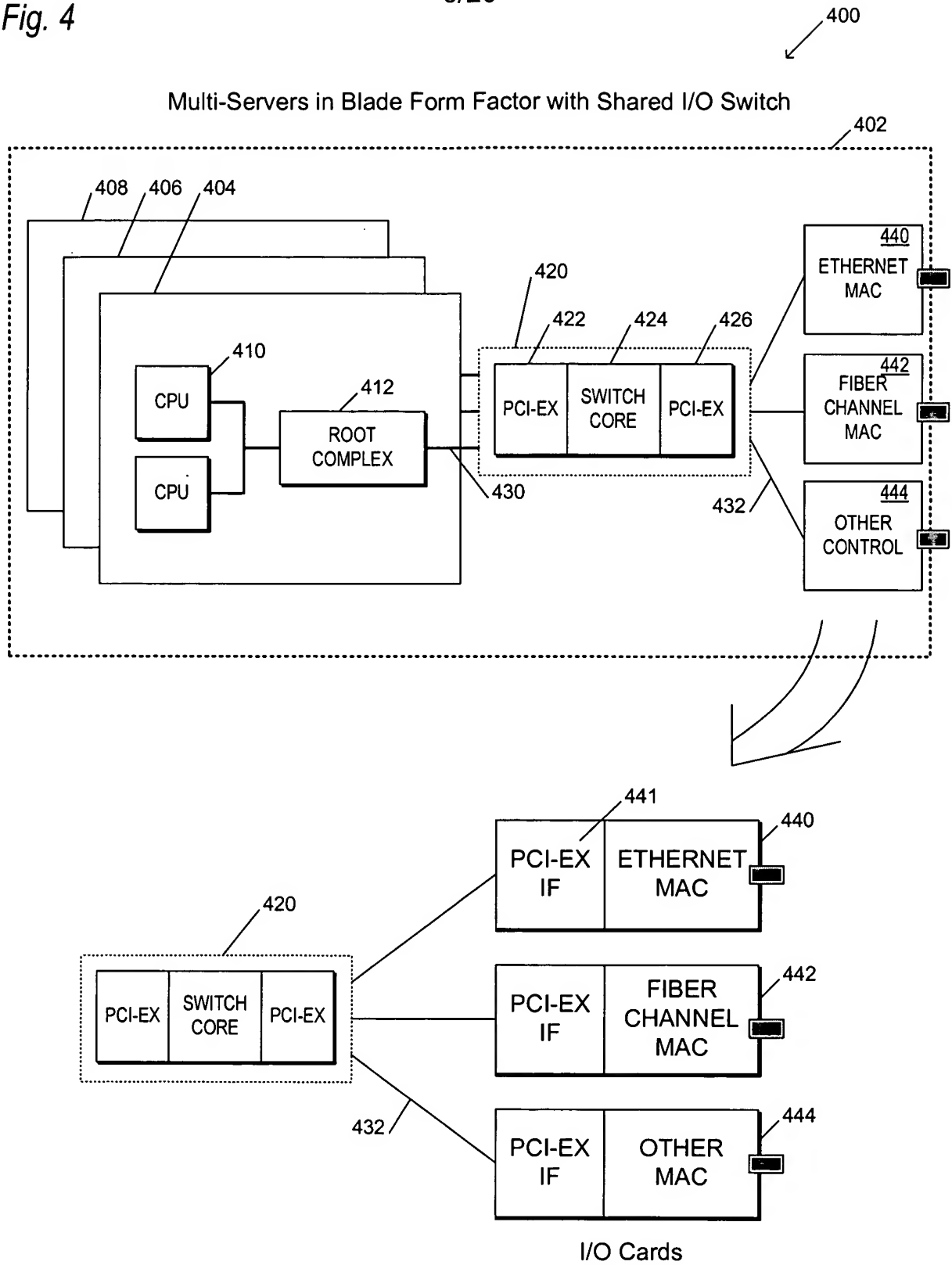


Fig. 5

MULTI-OPERATING SYSTEMS WITH SHARED I/O

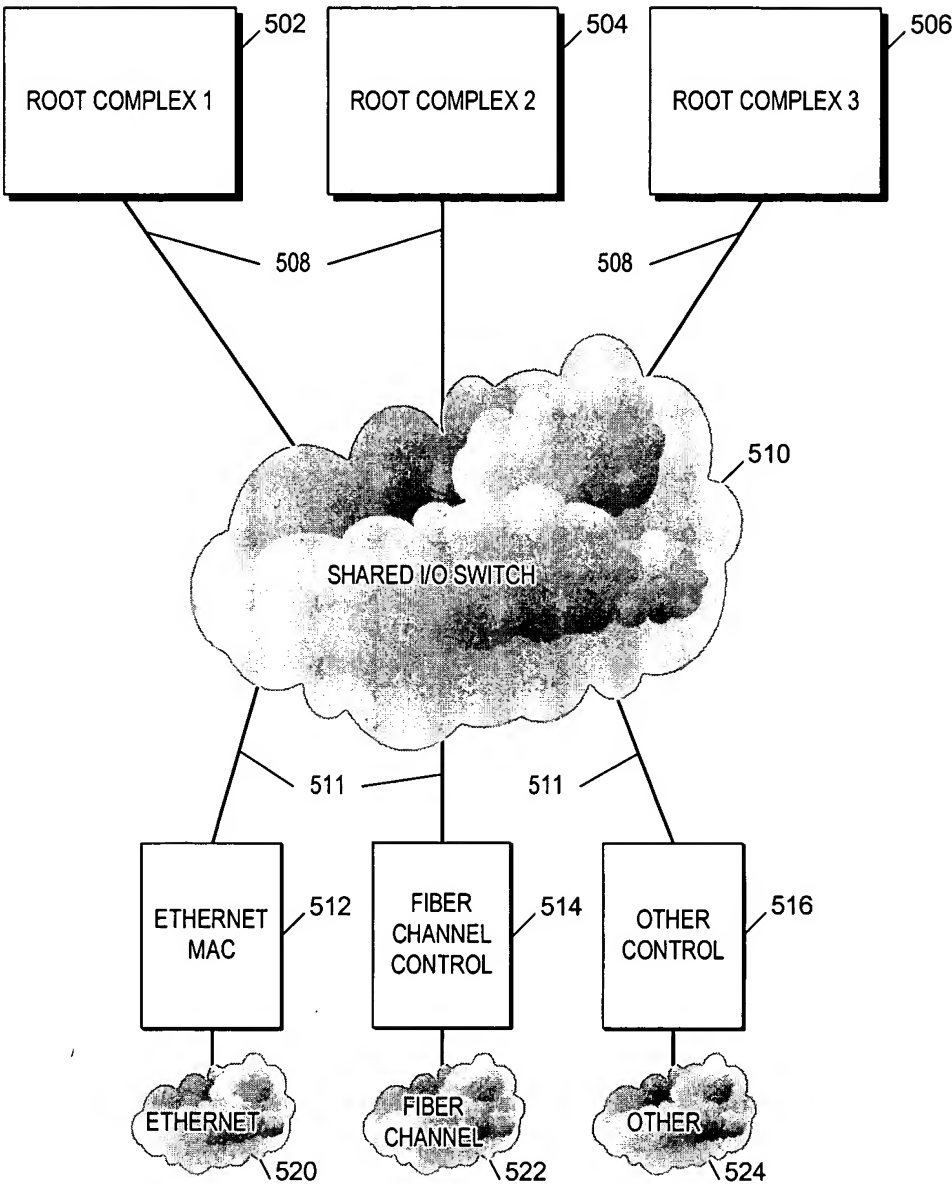


Fig. 6

MULTI-OPERATING SYSTEMS WITH SHARED ETHERNET CONTROLLER

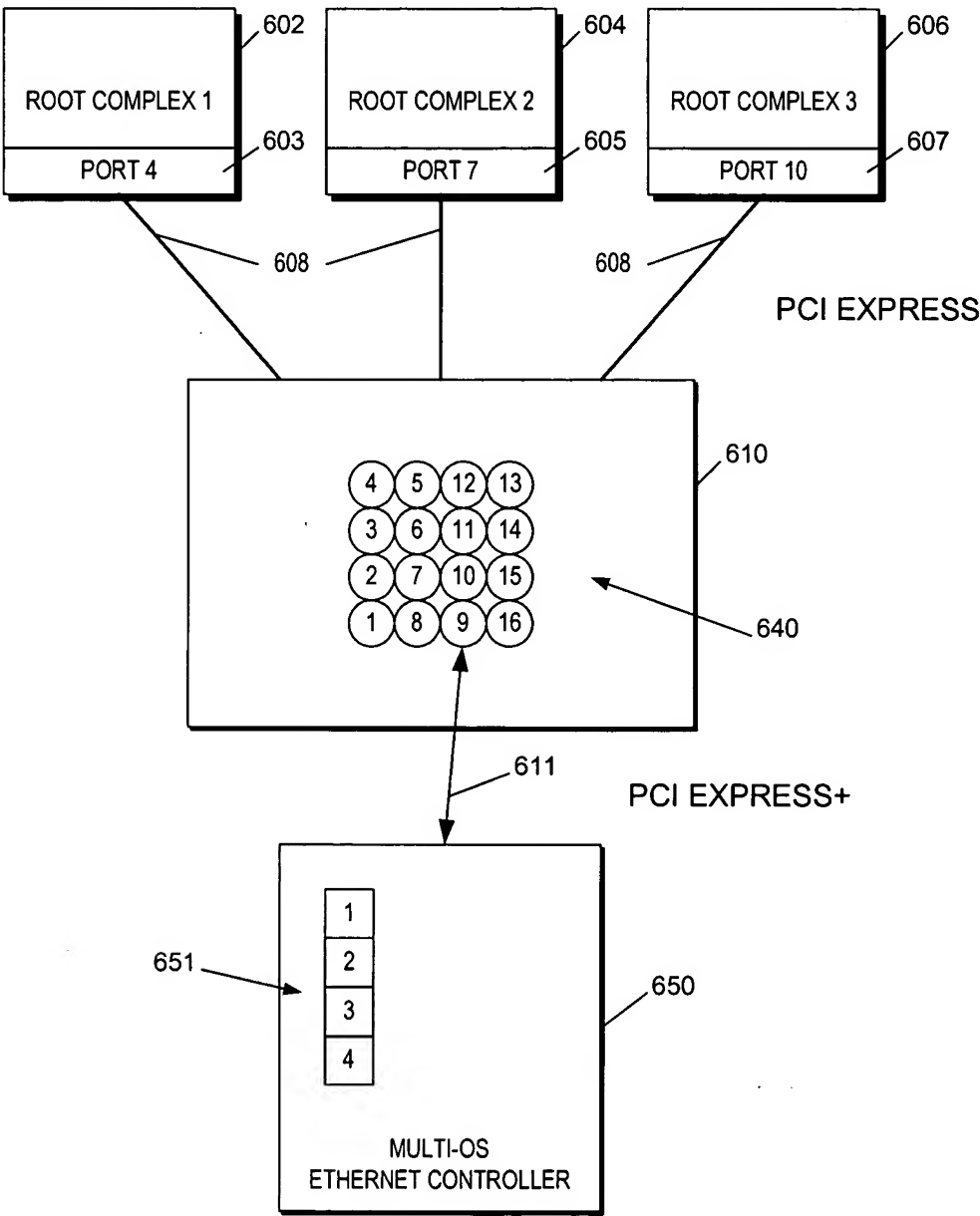


Fig. 7

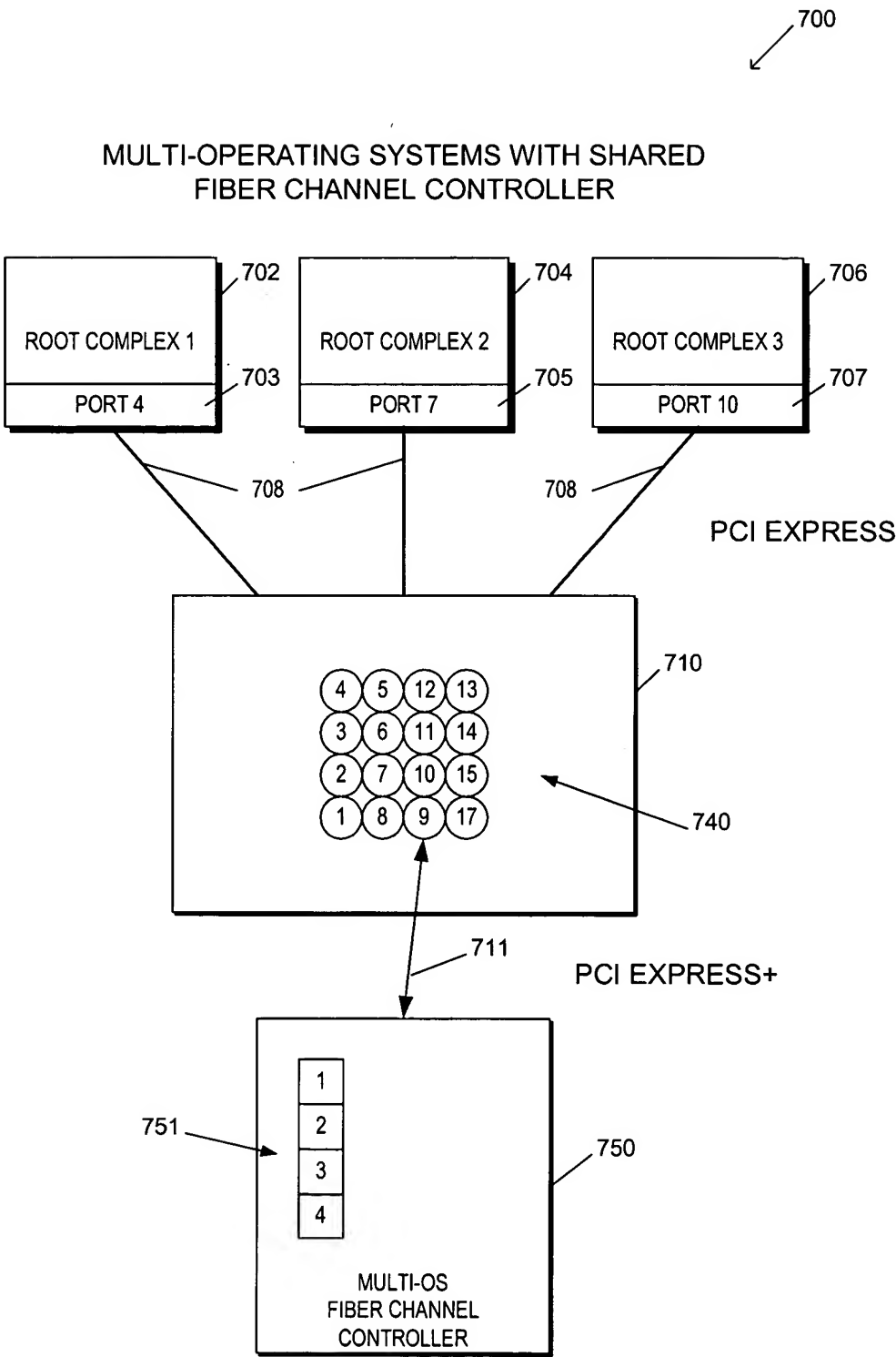


Fig. 8

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MULTI-OPERATING SYSTEMS WITH SHARED OTHER CONTROLLER

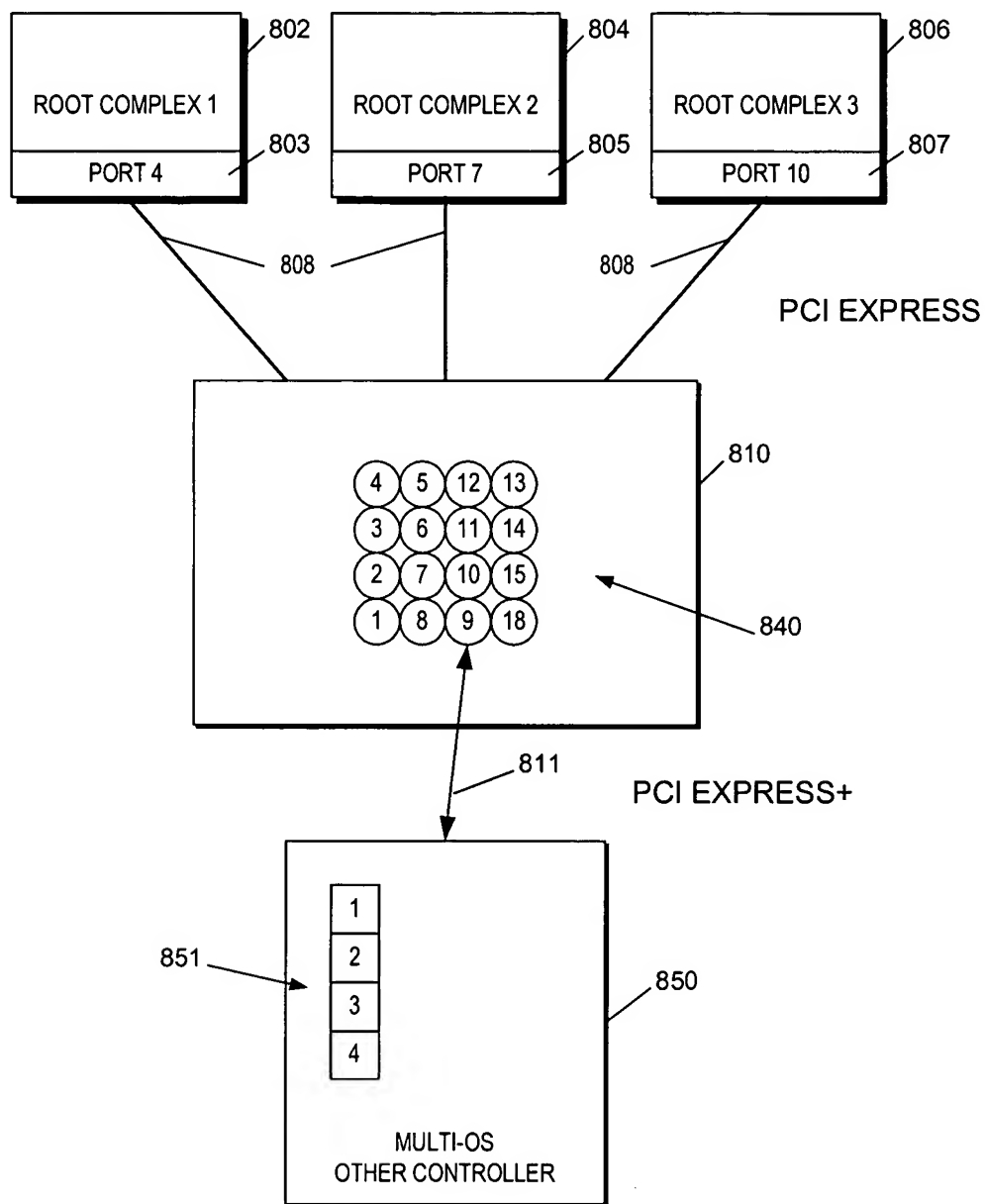


Fig. 9

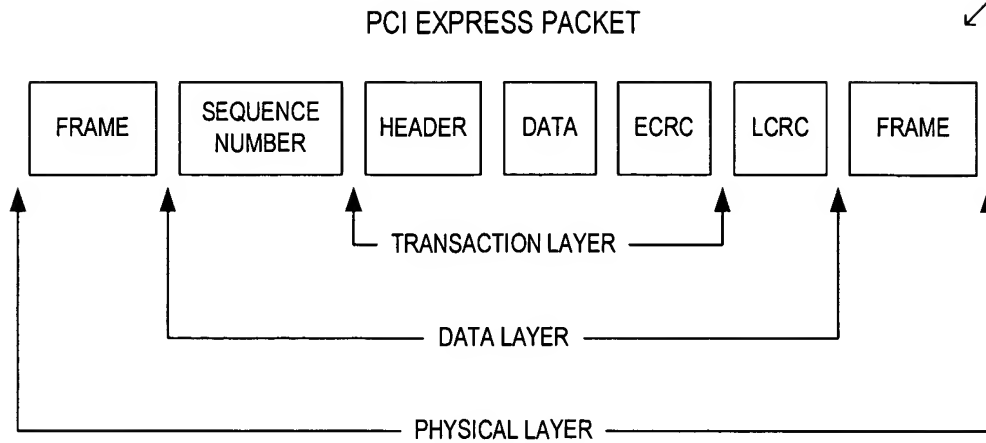


Fig. 10

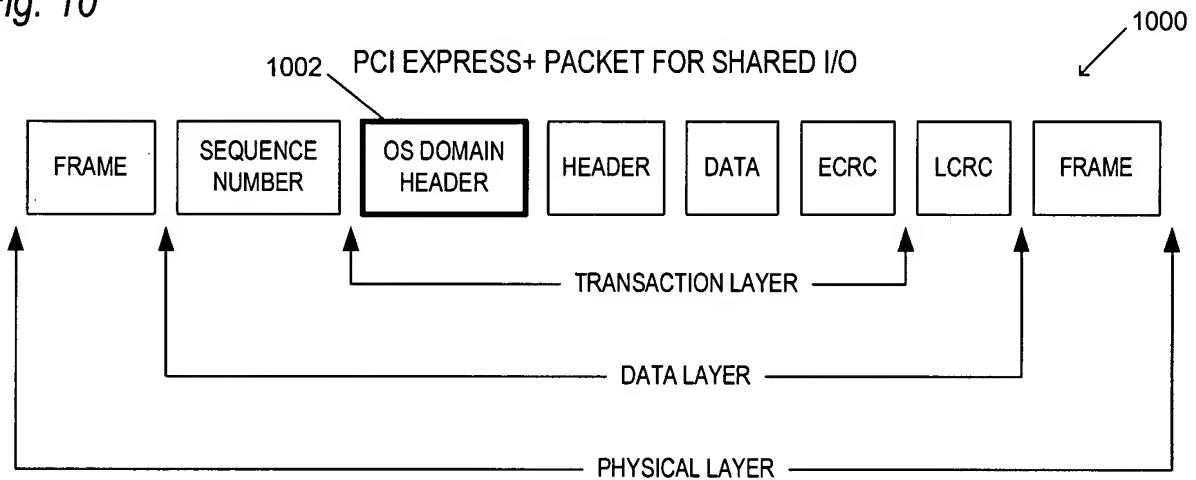
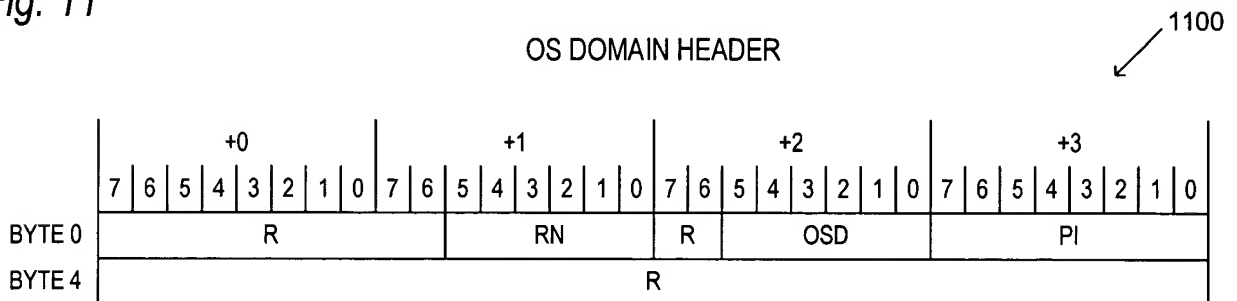


Fig. 11



- PI Protocol ID Field
- OSD OS Domain Number
- RN Resource Number (which buffer the packet belongs to)
- R reserved

Fig. 12 (Prior art)

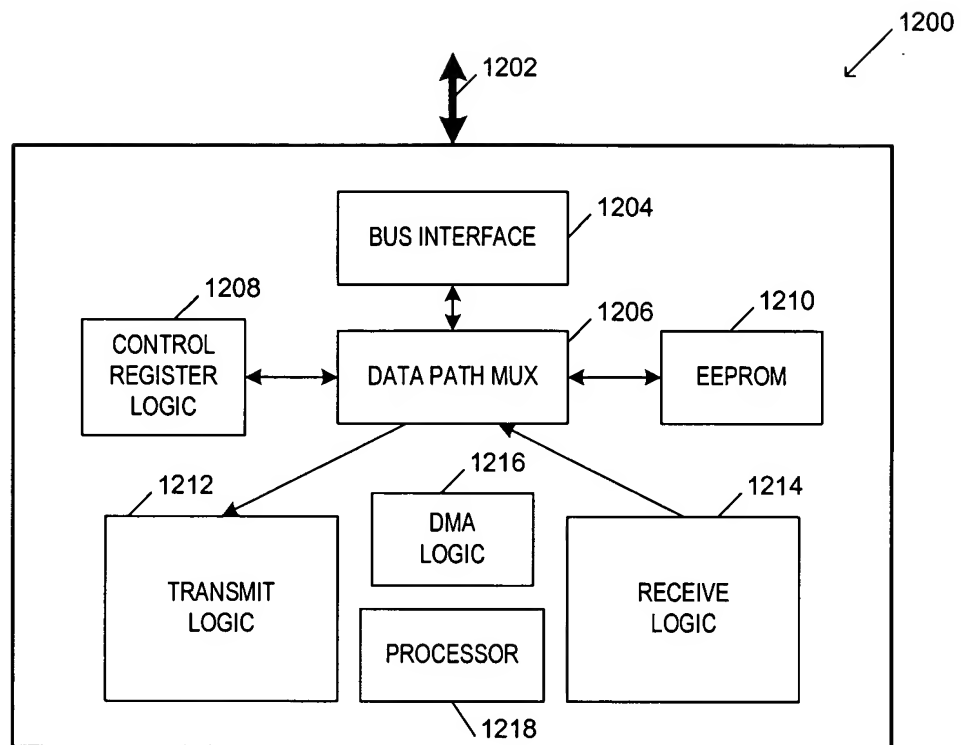


Fig. 13

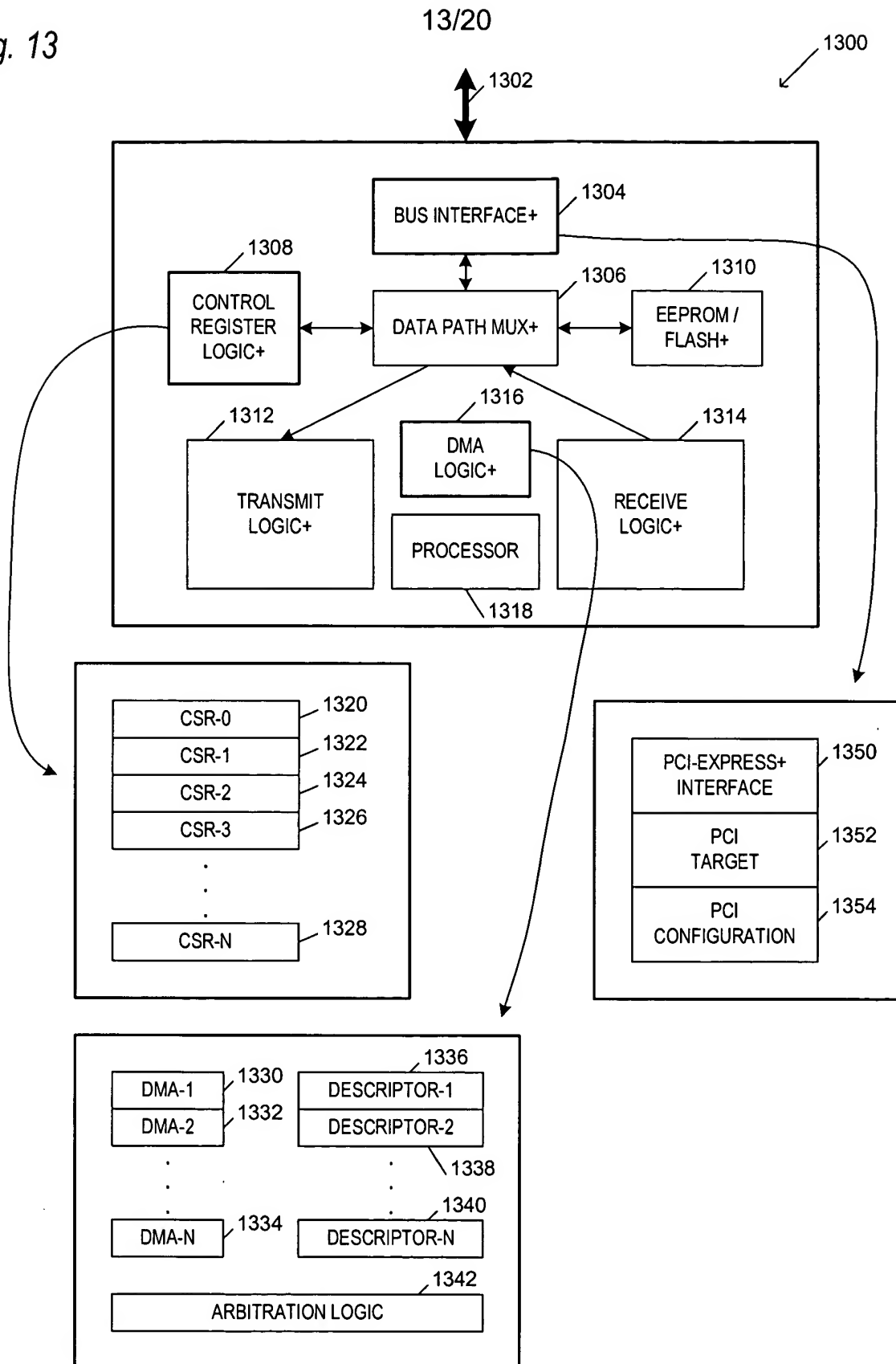


Fig. 14

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MULTI-OPERATING SYSTEMS WITH SHARED ETHERNET CONTROLLER
PACKET FLOW EXAMPLE

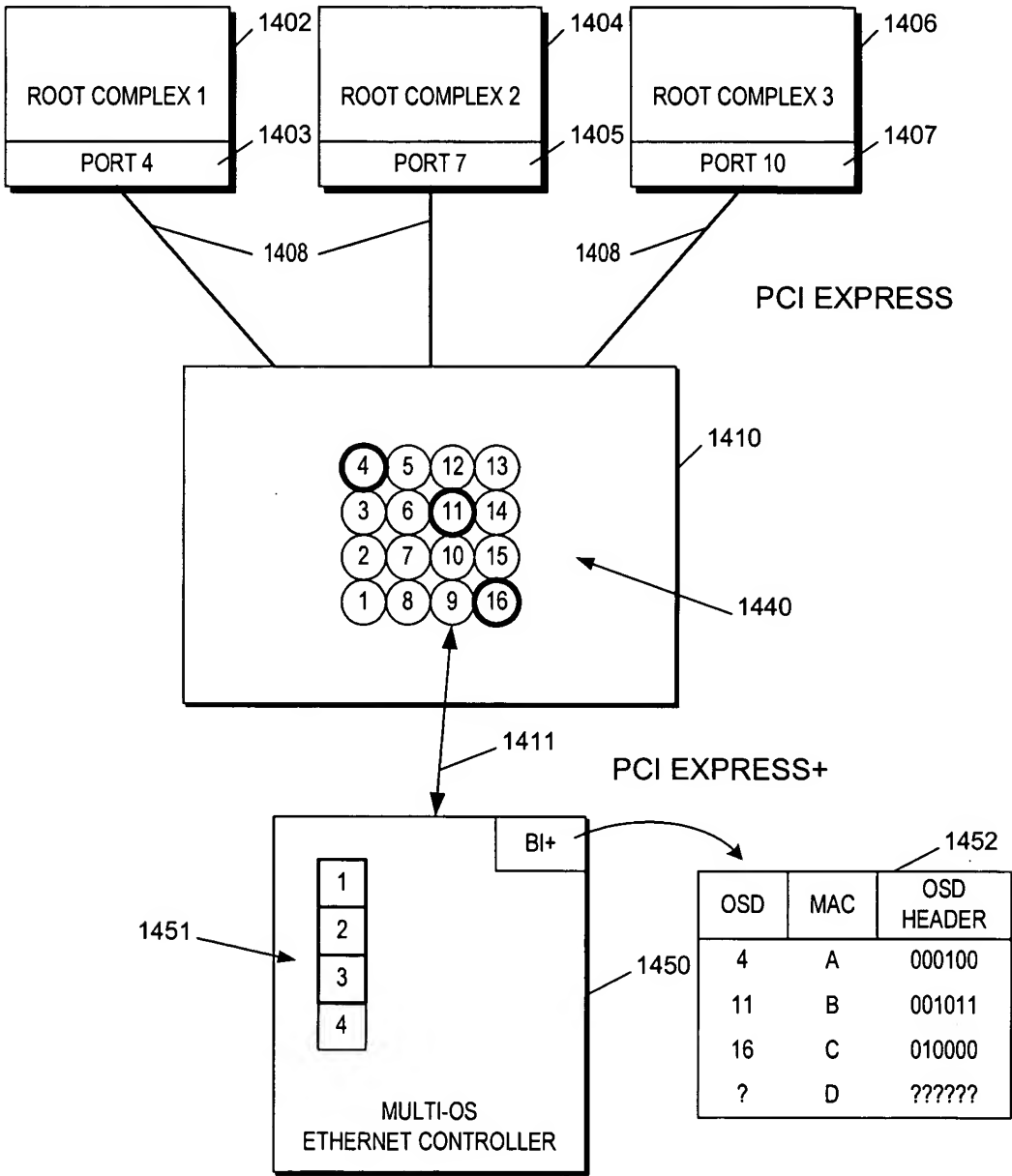


Fig. 15

METHOD OF SHARED I/O DOWNSTREAM TRANSMISSION FROM SWITCH

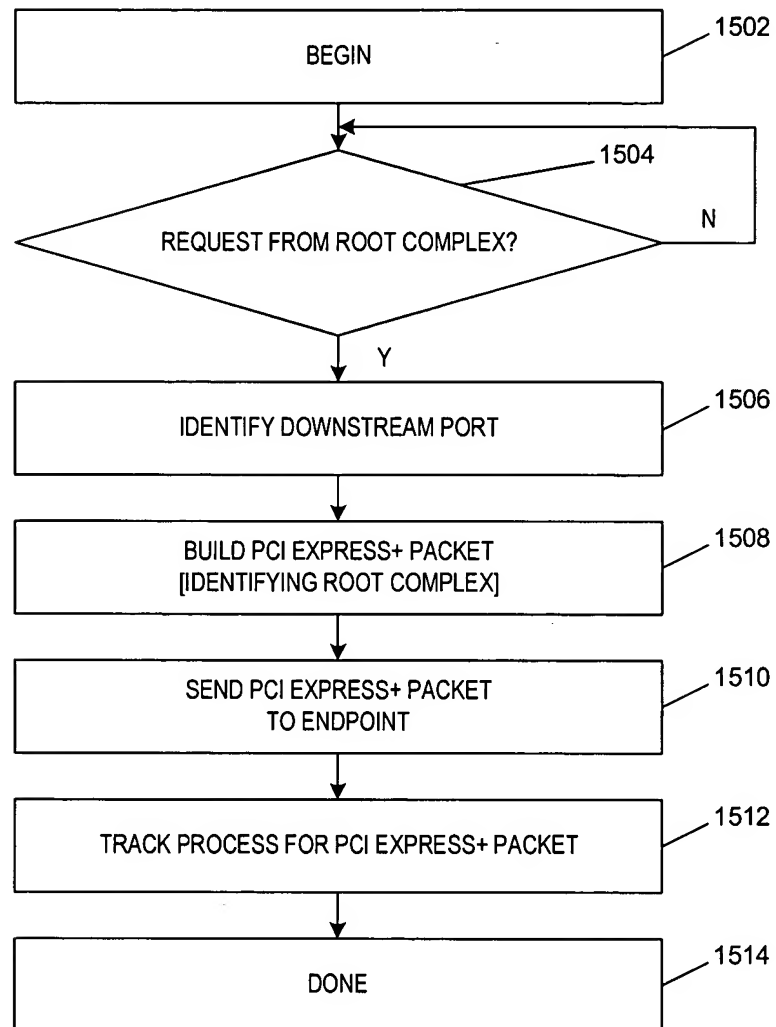


Fig. 16

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METHOD OF SHARED I/O UPSTREAM TRANSMISSION TO SWITCH

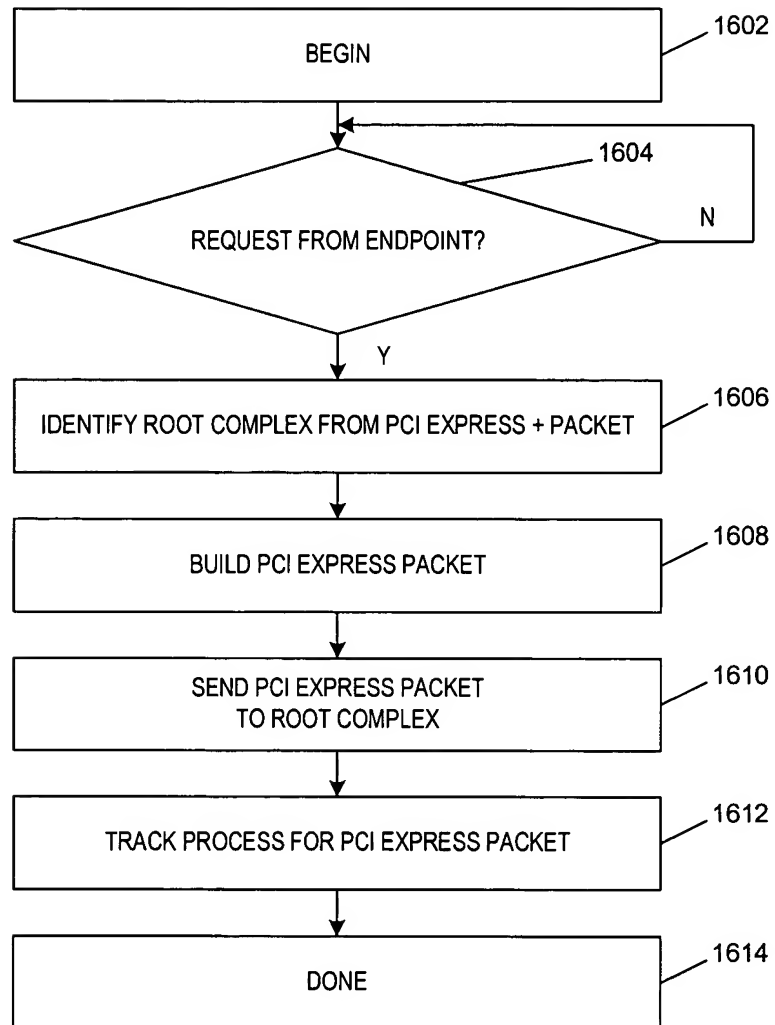


Fig. 17

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METHOD OF SHARED I/O DOWNSTREAM TRANSMISSION TO ENDPOINT

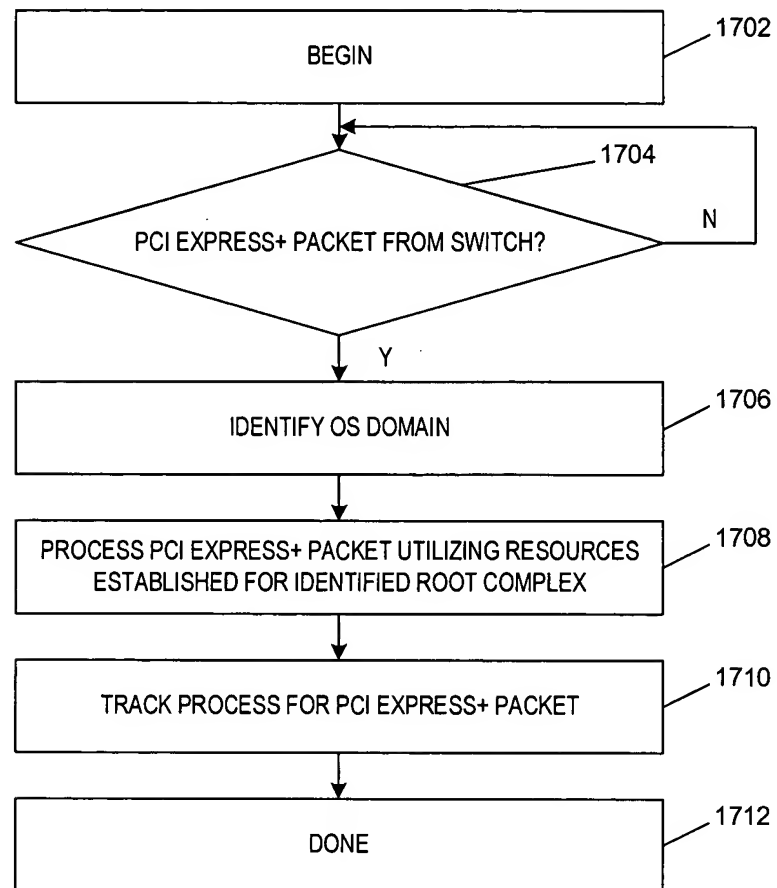


Fig. 18

METHOD OF SHARED I/O UPSTREAM TRANSMISSION FROM ENDPOINT

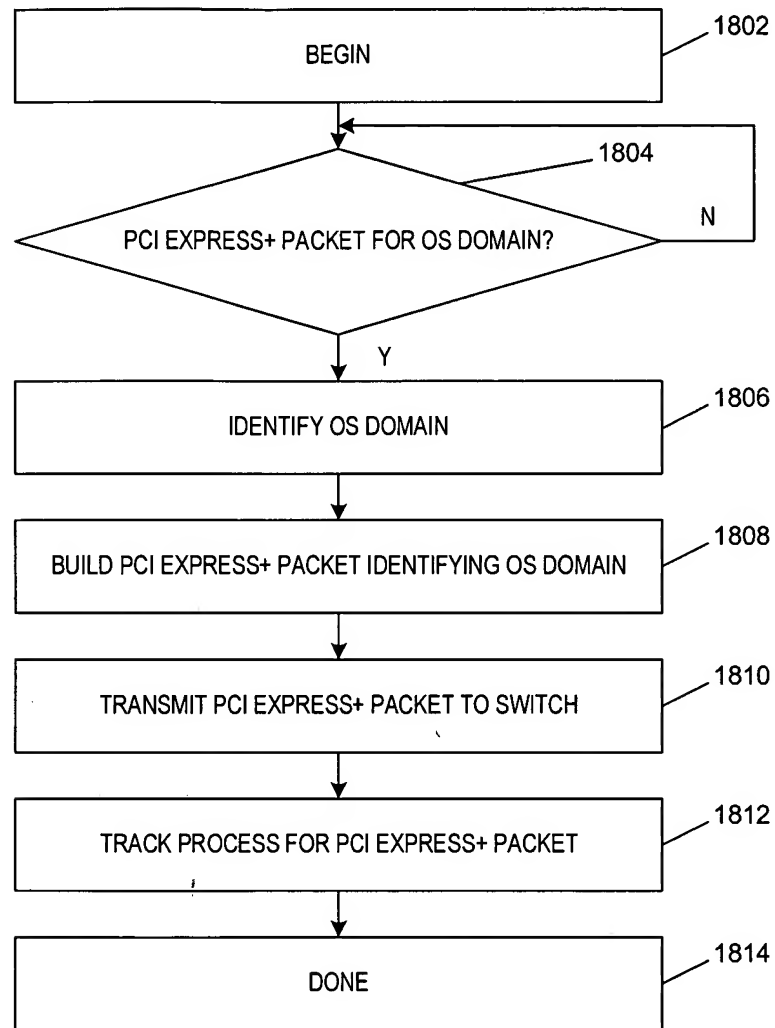
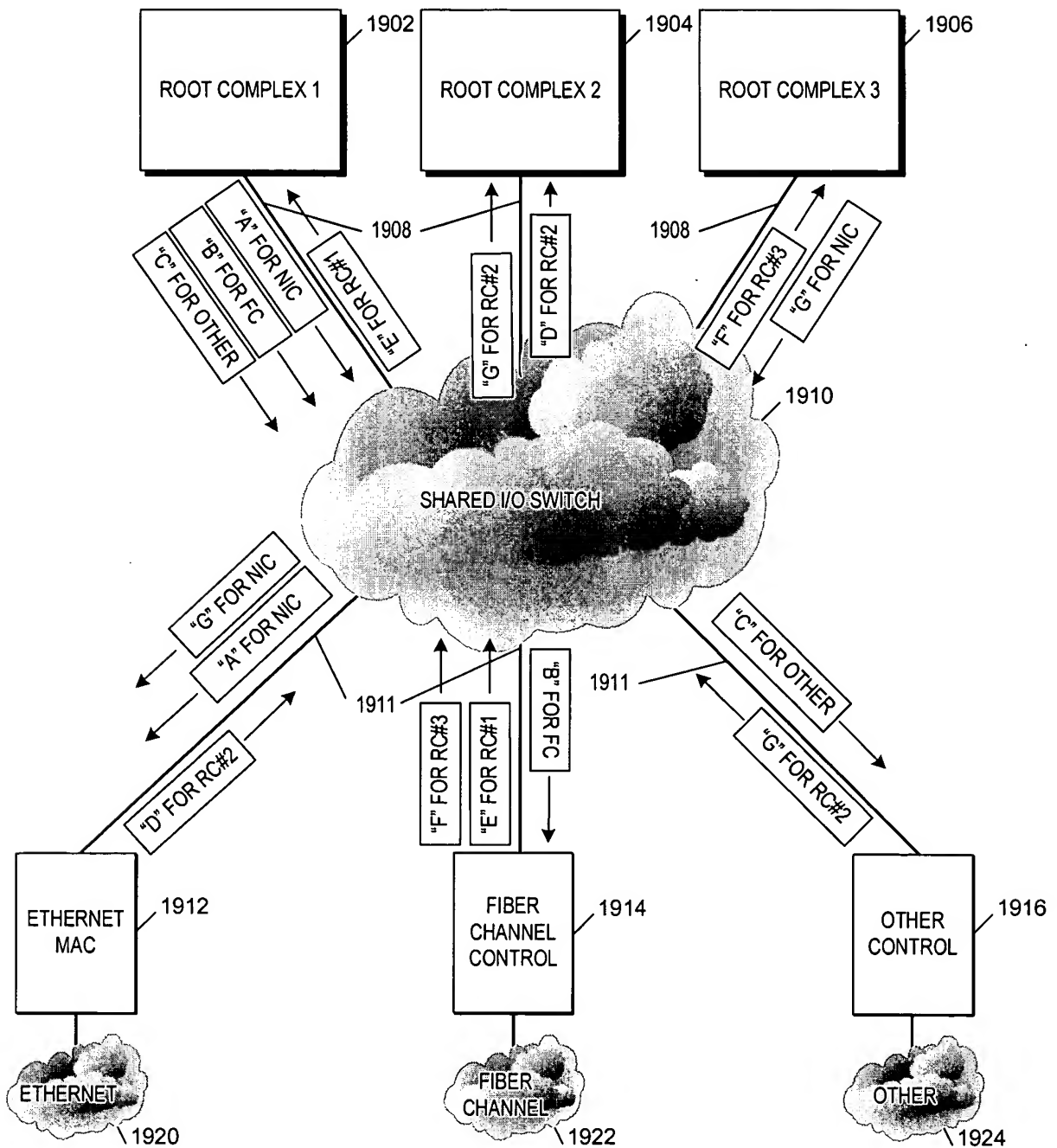


Fig. 19

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MULTI-OPERATING SYSTEMS WITH SHARED I/O



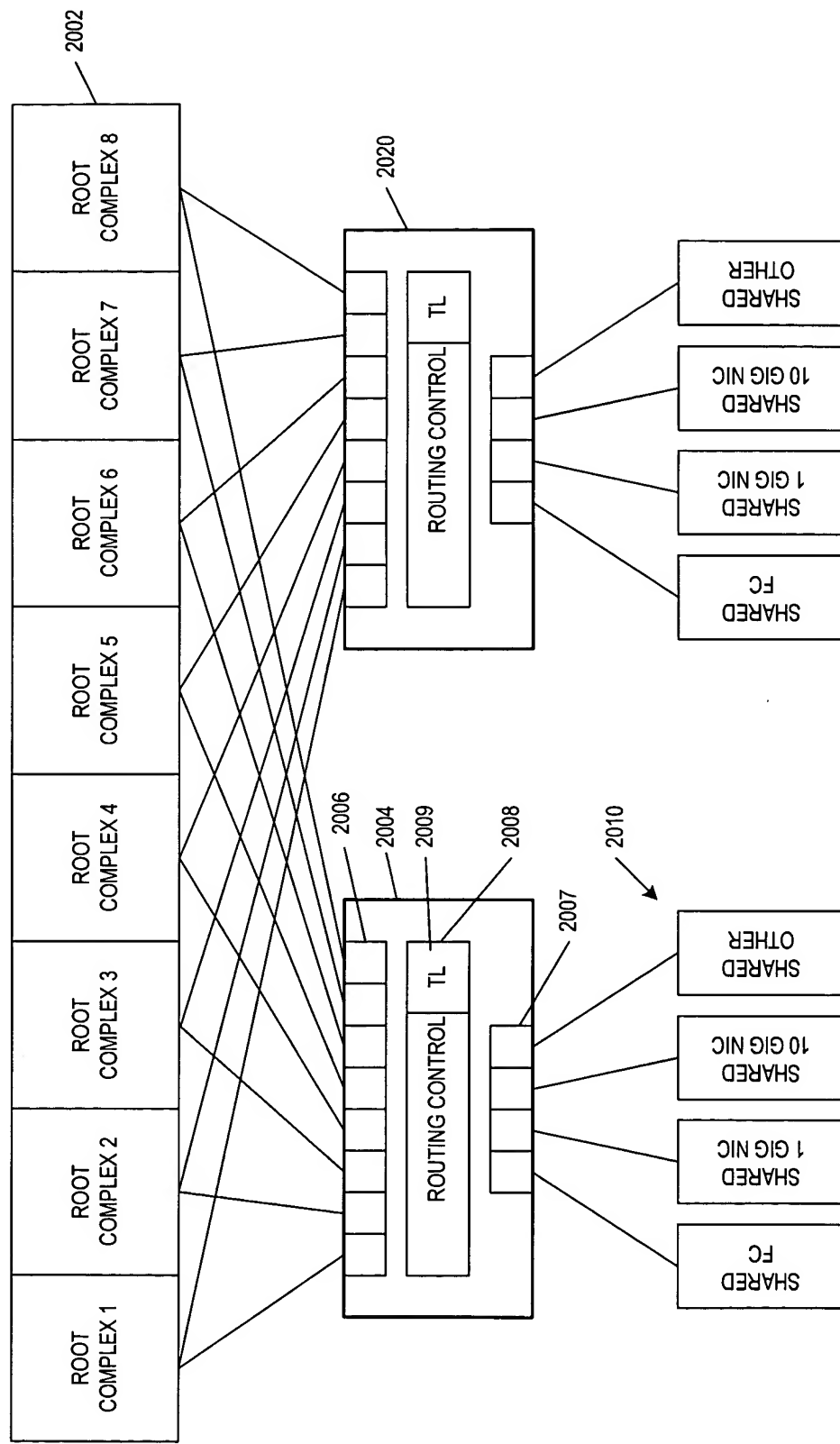
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Fig. 20

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2000

8 BLADE REDUNDANT ARCHITECTURE WITH SHARED I/O SWITCHES AND ENDPOINTS



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